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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814
Examiner: Phat Cao

In Re PATENT APPLICATION Of:

Applicant: KUMAMOTO et al.)
Serial No: 10/767,439)
Filed: January 30, 2004)
For: SEMICONDUCTOR CHIP AND) AMENDMENT
PRODUCTION PROCESS THEREFOR)
Docket No.: AI 318 D1)
December 13, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Examiner's Action dated September 22, 2005, please amend the application as follows: